



simulation circuit cycles derivative "peak aver...

Search

[Advanced Scholar Search](#)
[Scholar Preferences](#)
[Scholar Help](#)

Tip: Try removing quotes from your search to get more results.

Your search - **simulation circuit cycles derivative "peak average power"** - did not match any articles.

Suggestions:

- Make sure all words are spelled correctly.
- Try different keywords.
- Try more general keywords.
- Try fewer keywords.
- [Try your query on the entire web.](#)

[Google Home](#) - [About Google](#) - [About Google Scholar](#)

©2006 Google



Scholar All articles Recent articles Results 1 - 10 of about 15 for simulation circuit cycles "peak average power". (0.13 seconds)

All Results

M Hsiao

A Raghunathan

E Rudnick

J Patel

N Jha

Effects of Delay Models on Peak Power Estimation of VLSI Sequential Circuits - group of 15 »

MS Hsiao, EM Rudnick, JH Patel - Proc. International Conference on Computer Aided Design, 1997 - doi.ieeecs.org
 ... short execution times when compared with the random simulation. ... peak power dissipation
 of a circuit under different ... Peak power -cycle switching activity is a measure ...

Cited by 17 - Related Articles - Web Search - BL Direct

book High-Level Power Analysis and Optimization - group of 2 »

A Raghunathan, NK Jha, S Dey - 1997 - books.google.com
 ... Tied to peak/average power consumption ... is an application- specific integrated circuit (ASIC), which ... algorithmic descriptions, where the detailed cycle-by-cycle ...
 Cited by 109 - Related Articles - Web Search - Library Search

Power-time tradeoff in test scheduling for SoCs - group of 5 »

M Nourani, J Chin - Computer Design, 2003. Proceedings. 21st International ..., 2003 - ieeexplore.ieee.org
 ... the logic gate level, a circuit simulator computes the ... analytical computation to the original gate level simulation. ... equivalent to the number of cycles, in test ...
 Cited by 8 - Related Articles - Web Search

Power System Design for a Spacecraft Simulator using Energy Storage Flywheels - group of 3 »

BC Wilson, V Babuska, C Potter, JL Fausz - 3rd International Energy Conversion Engineering Conference, ..., 2005 - pdf.aiaa.org
 ... Laboratory AMPSS Agile Multipurpose Spacecraft Simulator ESCMG Energy ... peak power, high duty cycle, and long ... is designed to deliver the peak average power P max ...
 Cited by 2 - Related Articles - Web Search

Test scheduling with power-time tradeoff and hot-spot avoidance using MILP - group of 3 »

M Nourani, J Chin - Computers and Digital Techniques, IEE Proceedings-, 2004 - ieeexplore.ieee.org
 ... to account for power profile over cycle time and ... the logic-gate level, a circuit simulator computes the ... computation to their original gate-level simulation. ...
 Cited by 4 - Related Articles - Web Search - BL Direct

A H2 PEM Fuel Cell and High Energy Dense Battery Hybrid Energy Source for an Urban Electric Vehicle - group of 2 »

N Schofield, HT Yap, CM Bingham - Electric Machines and Drives, 2005 IEEE International ..., 2005 - ieeexplore.ieee.org
 ... Simulation of the ZEBRA battery is facilitated via ... model employing non-linear open-circuit terminal voltage ... frequency (<100Hz for the NEDC cycle), the battery ...
 Related Articles - Web Search

EXPERIMENTAL CONFIRMATION OF ON-SITE EFFICIENCY EVALUATION OF THREE-PHASE INDUCTION MOTORS USING ...

DJ McKinnon, C Grantham - itee.uq.edu.au
 ... must be noted that the equivalent circuit method does ... voltage over one synthetic loading cycle for the ... using computer modelling and simulation techniques, as ...
 Related Articles - View as HTML - Web Search

The effects of fault clearance practices on adjustable speed drives - group of 5 »

RQ Chen, SS Choi, M Vilathgamuwa - Advances in Power System Control, Operation and Management, ..., 1997 - ieeexplore.ieee.org
 ... Power supply is restored within several cycles after the fault ... 33)MVA 5. 1MVA
 Fig.2 Simulation network The ... is assumed to be equipped with circuit breakers of ...
 Related Articles - Web Search

book High-Level Power Analysis and Optimization

NK Jha, A Raghunathan, S Dey - 1998 - books.google.com
 ... is an application- specific integrated circuit (ASIC), which ... the cycle- by-cycle functionality specified ... leads to greatly reduced simulation and validation times ...
 Related Articles - Web Search

Effective IM2 estimation for two-tone and WCDMA modulated blockers in zero-IF

WY Ali-Ahmad - rfdesign.com
 ... with the proper use of silicon process, circuit design tech ... deviation of the LO

duty cycle from 50 ... An ADS IM2 simulation template was created to investigate ...
Related Articles - View as HTML - Web Search

Google ►
Result Page: 1 2 [Next](#)

simulation circuit cycles "peak avera Search

[Google Home](#) - [About Google](#) - [About Google Scholar](#)

©2006 Google